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*Kim*  
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**PATENT NUMBER and  
ISSUE DATE**

**U.S. UTILITY Patent Application**

A N	APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
	10084367	02/28/2002	438	197	2812	<i>202</i> <i>Forward</i>
<b>**APPLICANTS:</b> Nanjo-Ryota; Sugatani Shinji; Nakai Satoshi;						
<b>**CONTINUING DATA VERIFIED:</b> <i>JmJ</i> NONE						
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>JmJ</i> JAPAN 2001-067164 03/09/2001 JAPAN 2001-256108 08/27/2001						
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>		
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>JmJ</i>					ATTORNEY DOCKET NO 020200	
TITLE : Semiconductor device and its manufacture method						

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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